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Microprocessors and interfacing programming and hardware Author(S) Douglas V. Hall (Author) Publication Data New York: McGraw-Hill Book Company Publication€ Date 1986 Edition NA Physical Description vi, 554 p. : ill. ; 26 cm. Subject Computer Subject Headings Microprocessors Programming Microprocessors Computer interfaces

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are activated when IO/M signal is high, indicating I/O bus cycle. Reset out signal from 8085 is connected to the RESET signal of the 8255.

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